

Amendments to the Specification

Please replace the paragraph at page 17, line 25 to page 18, line 4 with the following amended paragraph:

In this embodiment, under execution of the normal carriage servo control, the control signal CLL from the CPU 31 causes both the switches 23 and 29 to make a switching operation as follows. The input terminal of the switch 23 is switched over to its first output terminal connecting to the averaging unit 24, and the output terminal of the switch 29 is switched over to its second input terminal connecting to the multiplier ~~29~~28.

Please replace the paragraph at page 20, lines 1-12 with the following amended paragraph:

On the other hand, the pulse producing unit 27 continuously produces a pulse signal ~~PQ~~ PS whose period of time T is constant and whose duty ratio is also constant (refer to the bottom left in FIG. 3). The constant period T is previously determined such that the carriage motor 8 is driven in a stabilized manner and under the state based on designed specifications. The period of the pulse signal PS (refer to the second waveform from the bottom left in FIG. 3) is sufficiently shorter than one period of the averaged equalizer signal VQ and corresponds to operating characteristics of a feed mechanism of the carriage CG in which the carriage motor 8 and the shaft 12 are disposed. The pulse producing unit 27 provides the duty ratio control unit 33 with the produced pulse signal PS.